

FIG. 1A

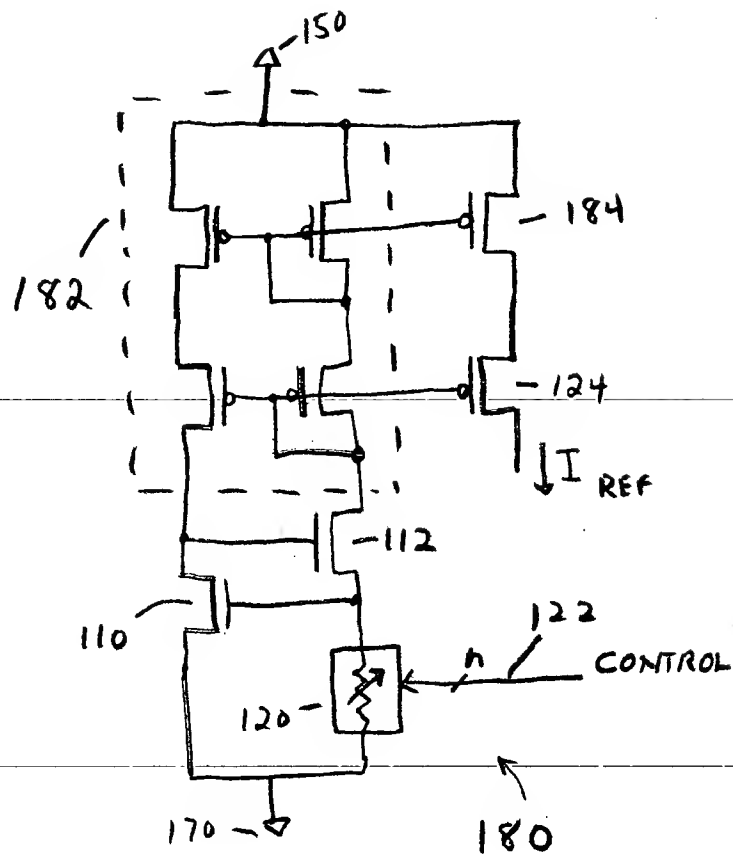


FIG. 1B

FIG. 2 is a schematic diagram of a circuit for generating a sequence of signals. The circuit includes a series of resistors (214, 218, 222, 226, 230) and capacitors (212, 216, 220, 224, 228) connected in a ladder network. The output signals are labeled N[0], N[1], N[2], N[3], and N[4]. The circuit is powered by a supply voltage 250 and a reference voltage 200.

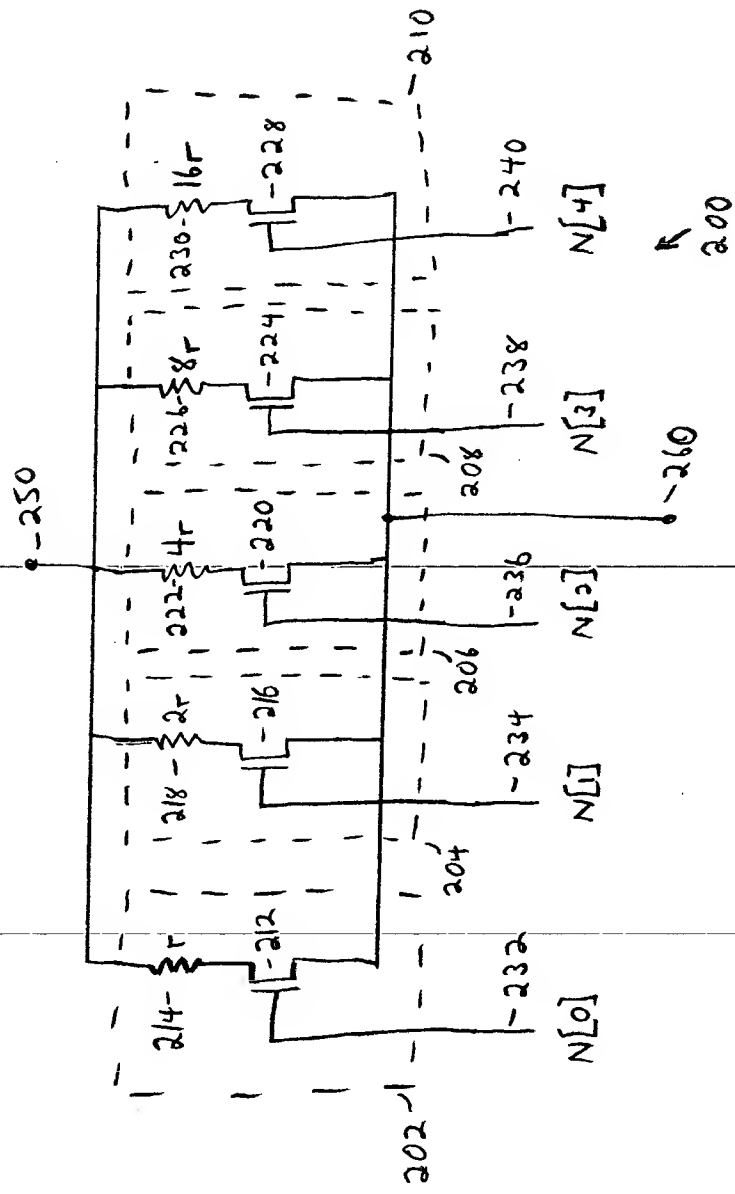


FIG. 2

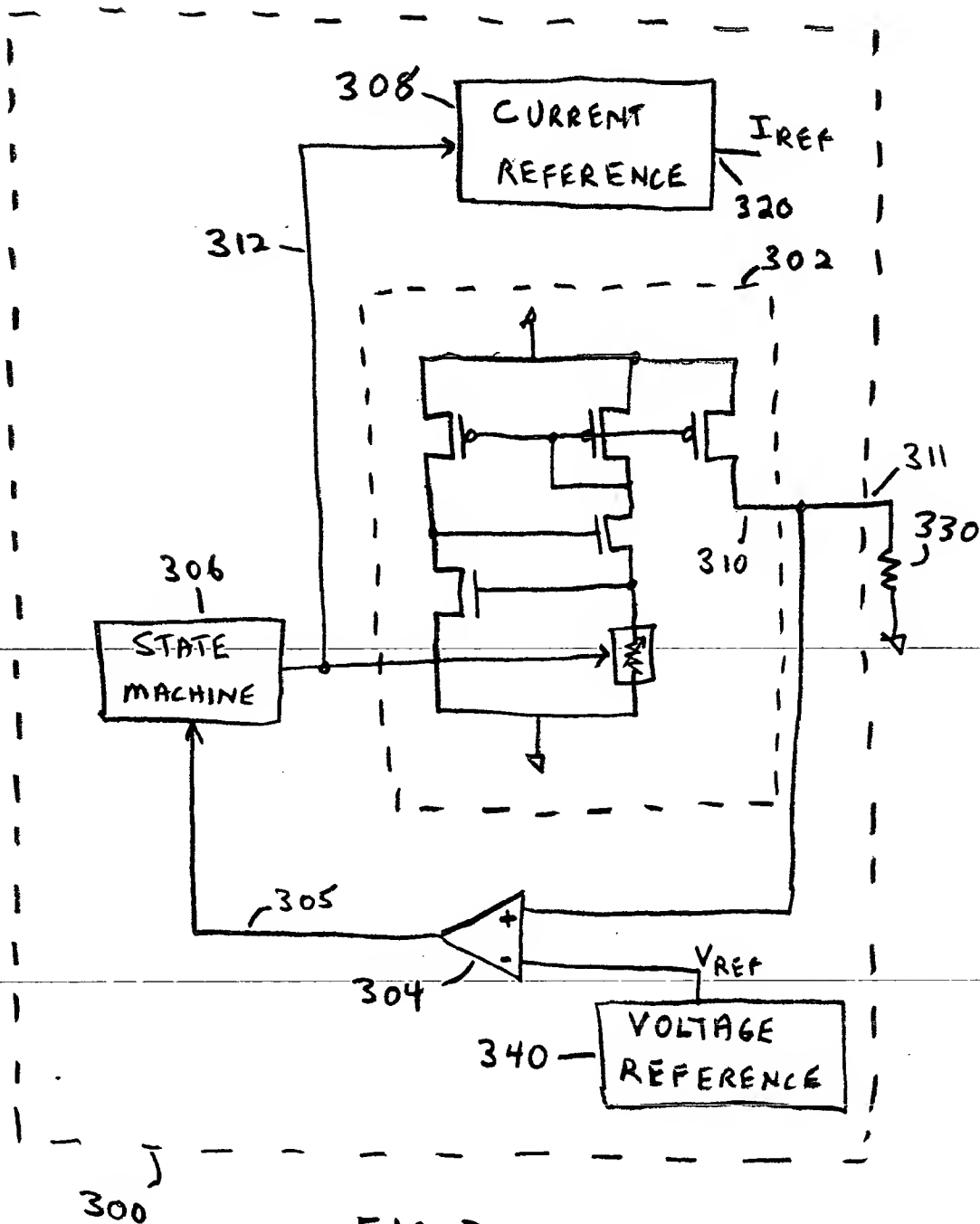


FIG. 3

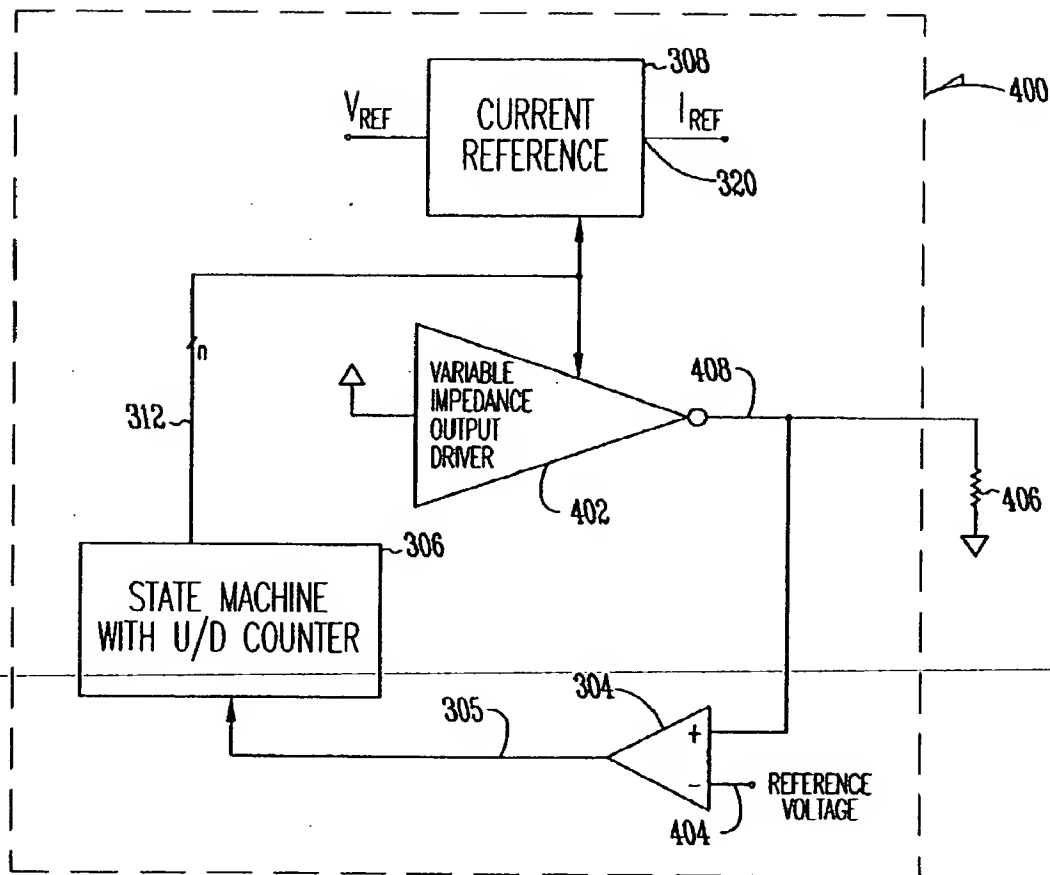


Fig. 4

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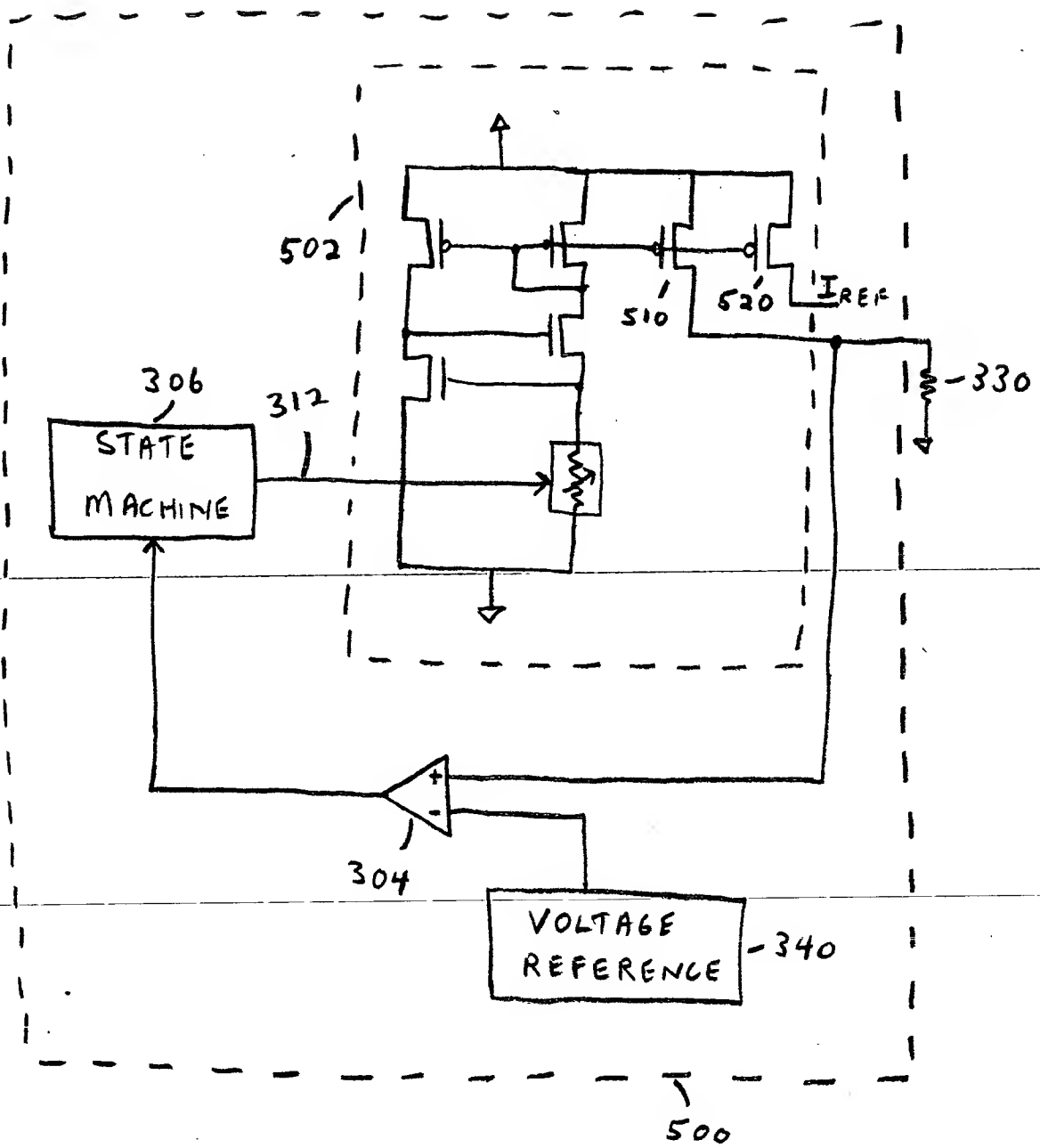


FIG. 5